

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (currently amended): A base station ~~or a mobile station~~ of a radio-operated telecommunications system ~~with a receiver for processing received information comprising:~~
a receiver processing received information; and with
at least one digital signal processor, ~~in particular a digital signal processor, configured to~~
~~for performing a symbol rate processing, the signal processor being and~~ suitable for and provided
~~for likewise performing~~ at least parts of a chip rate processing.
2. (currently amended): ~~A~~ The base station ~~or a mobile station~~ as claimed in Claim 1, wherein the signal processor is also configured to being suitable for performing a task allocation for controlling the chip rate processing and the symbol rate processing.
3. (original): ~~A~~ The base station ~~or a mobile station~~ as claimed in Claim 1, the signal processor being designed such that firstly the chip rate processing and then the symbol rate processing can be performed.
4. (original): ~~A~~ The base station ~~or mobile station~~ as claimed in Claim 1, wherein an array or group of digital signal processors is provided.

5. (original): ~~A~~The base station ~~or a mobile station~~ as claimed in Claim 4, wherein the chip rate processing and the symbol rate processing can be distributed between sub-arrays or sub-groups of signal processors.

6. (original): ~~A~~The base station ~~or a mobile station~~ as claimed in Claim 1, wherein at least one memory is provided which is suitable for and provided for the intermediate storage of the received information.

7. (currently amended): The ~~A~~ base station ~~or a mobile station~~ as claimed in Claim 1, wherein the chip rate processing ~~is comprising~~ a despreading of the received information and wherein the signal processor is configured to dispread the received information.

8. (currently amended): The ~~A~~ base station ~~or a mobile station~~ as claimed in Claim 1, wherein the symbol rate processing ~~comprising~~ a decoding of the received information.

9. (currently amended): A receiver for a base station ~~or a mobile station~~ of a radio-operated telecommunications system for processing received information with at least one digital signal processor, ~~in particular a digital signal processor~~, for performing a symbol rate processing, wherein the signal processor is ~~suitable for and provided for likewise~~ also configured for performing at least parts of a chip rate processing.

10. (currently amended): A ~~radio-operated telecommunications system,~~ digital signal processor configured to execute symbol rate processing for a receiver of a base station of a radio-operated telecommunications system, wherein the signal processor is configured to perform at least parts of a chip rate processing~~wherein a base station or a mobile station as claimed in Claim 1 is provided.~~

11. (currently amended): A radio-operated telecommunications system comprising at least one of:~~as claimed in Claim 10, consisting of~~

a base station having at least one digital signal processor configured to perform a symbol rate processing and at least parts of a chip rate processing; and

a receiver processing received information having said at least one digital signal processor; and

said at least one digital processor~~a code division multiple access (CDMA) telecommunications system.~~

12. (currently amended): A process for operating a radio-operated telecommunications system, wherein information received by a base station ~~or a mobile station is~~ subjected to a symbol rate processing by ~~means of~~ at least one digital signal processor, ~~in particular a digital signal processor~~, wherein at least a part of the chip rate processing is likewise performed by the at least one digital signal processor.

13. (original): The ~~A~~-process as claimed in Claim 12, wherein firstly the chip rate processing and then the symbol rate processing is performed.

14. (original): ~~A~~-The process as claimed in Claim 12, wherein a task allocation for controlling the chip rate processing and the symbol rate processing is performed by the at least one signal processor.

15. (original): ~~A~~-The process as claimed in Claim 12, wherein an array or group of digital signal processors is provided, the chip rate processing and the symbol rate processing is distributed between sub-arrays or sub-groups of signal processors.

16. (original): ~~A~~-The process as claimed in Claim 15, wherein the distribution of the array or group of signal processors between the chip rate processing and the symbol rate processing is performed by the task allocation.

17. and 18. (canceled).

19. (new): The telecommunication system according to claim 11, wherein the telecommunication system is a code division multiple access (CDMA) telecommunications system.

20. (new): A digital signal processor comprising:

means for executing symbol rate processing;
means for executing chip rate processing; and
means for switching over from said means for executing symbol rate processing to said
means for executing chip rate processing,
wherein the digital signal processor is disposed inside a receiver.

21. (new): The digital signal processor according to claim 20, wherein the means for switching instructs for transmission of information in the digital processor first to the means for executing chip rate processing and then to the means for executing symbol rate processing.